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L9 and memory	1

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<u>L10</u>	L9 and memory	1	<u>L10</u>
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<u>L7</u>	6191758	1	<u>L7</u>
<u>L6</u>	L4 and auxiliary	0	<u>L6</u>
<u>L5</u>	L4 and pixel	0	<u>L5</u>
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<u>L3</u>	6295038	2	<u>L3</u>
<u>L2</u>	L1 and photomask	4	<u>L2</u>
<u>L1</u>	((345/107)!.CCLS.) and electrophoretic	127	<u>L1</u>

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Terms	Documents
L23 and (microcontroller or microprocessor)	6

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<u>L25</u>	L23 and (microcontroller or microprocessor)	6	<u>L25</u>
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<u>L13</u>	((345/1.1)!.CCLS.)	181	<u>L13</u>
<u>L12</u>	L10 and cable	0	<u>L12</u>
<u>L11</u>	L10 and ribbon	0	<u>L11</u>
<u>L10</u>	L9 and memory	1	<u>L10</u>
<u>L9</u>	L8 and interface	1	<u>L9</u>
<u>L8</u>	L7 and pixel	1	<u>L8</u>
<u>L7</u>	6191758	1	<u>L7</u>
<u>L6</u>	L4 and auxiliary	0	<u>L6</u>
<u>L5</u>	L4 and pixel	0	<u>L5</u>
<u>L4</u>	L3 and memory	2	<u>L4</u>
<u>L3</u>	6295038	2	<u>L3</u>
<u>L2</u>	L1 and photomask	4	<u>L2</u>
<u>L1</u>	((345/107)!.CCLS.) and electrophoretic	127	<u>L1</u>

END OF SEARCH HISTORY